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## FOREWORD

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### Special Section on Circuits and Systems

The 34th Workshop on Circuits and Systems was held successfully during August 26th to 27th, 2021. Although the Workshop was planned to be held in Kitakyushu, it was held as an online conference due to influence of COVID-19. 54 selected papers and 6 invited talks and one special invited lecture was presented in the workshop. They were focused both on fundamental and application aspects of hot topics in circuits and systems including signal processing, VLSI design technologies, and mathematical systems science. The workshop has been organized and run by the workshop committee of 52 young and active researchers and engineers, which was supported by IEICE Engineering Sciences Society (ESS), System and Signal Processing Subociety. The success of the 34th Workshop on Circuits and Systems was totally realized by their contribution.

After the workshop, the editorial committee called for papers which focused on the same fields of the workshop for this special section. The aim of the special section is to spread the state-of-the-art of technologies emerging from the workshop and to promote future research. Papers presented at the workshop were welcomed but this special section also was opened to all researchers in the related area. Four interesting full papers were accepted for publication out of 8 submitted full papers and one letter.

As the Guest Editor-in-Chief, I greatly appreciate their effort of the editorial committee members of this special section, especially to the Guest Editors, Dr. Tomoyuki Yokogawa and Dr. Takeshi Matsumoto. I would also like to thank the reviewers for their cooperation in spite of a limited time due to the tight schedule for special section. Finally I would like to express my deep thanks to all the authors who submitted papers to this special section.

I hope that you will enjoy the world of the latest technology through this special section.

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Takahide Sato, Guest Editor-in-Chief

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**Takahide Sato** (*Member*) received his Master and Doctor of engineering degree from Tokyo Institute of Technology at 2000 and 2005 respectively. From 2002 through 2009, he was with Tokyo Institute of Technology, as an assistant professor. In 2009, he joined University of Yamanashi, where he is currently as a professor. His research interests are in analog integrated circuits design, especially in biomedical circuits and dc-dc converters. He is a member of IEEE and IEEJ.

